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Inventor Information for 10/086594

Inventor Name	City	State/Country
HAQ, EJAZ UL	SUNNYVALE	CALIFORNIA
SLAGER, JAMES R.	SARATOGA	CALIFORNIA

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Inventor Name Search Result

Your Search was:

Last Name = HAQ
First Name = EJAZ

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08638100	5737258	150	04/26/1996	NONVOLATILE SEMICONDUCTOR MEMORY WHICH IS CONNECTABLE TO A DRAM BUS	HAQ, EJAZ
08739276	5732018	150	10/29/1996	SELF-CONTAINED REPROGRAMMING NONVOLATILE INTEGRATED CIRCUIT MEMORY DEVICES AND METHODS	HAQ, EJAZ
08768271	Not Issued	161	12/17/1996	SENSE AMPLIFIER CIRCUIT FOR SEMICONDUCTOR MEMORY DEVICE	HAQ, EJAZ
07456879	4973865	250	12/20/1989	AUTO-DELAY GAIN CIRCUIT	HAQ, EJAZ U.
07534809	Not Issued	166	06/07/1990	INTEGRATED CIRCUIT MEMORY WITH NON-BINARY ARRAY CONFIGURATION	HAQ, EJAZ U.
07783040	5220518	150	10/25/1991	INTEGRATED CIRCUIT MEMORY WITH NON-BINARY ARRAY CONFIGURATION	HAQ, EJAZ U.
08380975	Not Issued	162	01/31/1995	SEMICONDUCTOR MEMORY DEVICE HAVING LOW POWER SELF REFRESH AND BURN-IN FUNCTIONS	HAQ, EJAZ U.
08580645	5636171	150	12/29/1995	SEMICONDUCTOR MEMORY DEVICE HAVING LOW POWER SELF REFRESH AND BURN-IN FUNCTIONS	HAQ, EJAZ U.
08705040	5654930	150	08/30/1996	SEMICONDUCTOR MEMORY DEVICE HAVING LOW POWER SELF REFRESH AND BURN-IN FUNCTIONS	HAQ, EJAZ UI
60078213	Not Issued	159	03/16/1998	HIGH SPEED SOURCE SYNCHRONOUS SIGNALING	HAQ, EJAZ UI

				FOR INTERFACING VLSI CMOS CIRCUITS TO TRANSMISSION LINES	
09576109	Not Issued	168	05/22/2000	Signal latching of high bandwidth dram arrays when skew between different components is higher than signal rate	HAQ, EJAZ UL
09578354	6327205	150	05/24/2000	Signal latching of high bandwidth dram arrays when skew between different components in higher than signal rate	HAQ, EJAZ UL
09637338	6513080	150	08/10/2000	HIGH SPEED BUS SYSTEM AND METHOD FOR USING VOLTAGE AND TIMING OSCILLATING REFERENCES FOR SIGNAL DETECTION	HAQ, EJAZ UL
09851622	6812767	150	05/08/2001	HIGH SPEED SOURCE SYNCHRONOUS SIGNALING FOR INTERFACING VLSI CMOS CIRCUITS TO TRANSMISSION LINES	HAQ, EJAZ UL
10086594	Not Issued	30	02/27/2002	Method and system for deskewing parallel bus channels to increase data transfer rates	HAQ, EJAZ UL
10159488	Not Issued	41	05/31/2002	System and method for increasing signal strength at a receiver in transmission lines with high attenuation	HAQ, EJAZ UL
10947892	7009428	150	09/22/2004	HIGH SPEED SOURCE SYNCHRONOUS SIGNALING FOR INTERFACING VLSI CMOS CIRCUITS TO TRANSMISSION LINES	HAQ, EJAZ UL
11176439	Not Issued	41	07/06/2005	High speed source synchronous signaling for interfacing VLSI CMOS circuits to transmission lines	HAQ, EJAZ UL
11176799	Not Issued	93	07/06/2005	HIGH SPEED SOURCE SYNCHRONOUS SIGNALING FOR INTERFACING VLSI CMOS CIRCUITS TO TRANSMISSION LINES	HAQ, EJAZ UL
60180236	Not Issued	159	02/04/2000	Signal latching of high bandwidth dram arrays when skew between different components is higher than signal rate	HAQ, EJAZ UL

60272222	Not Issued	159	02/27/2001	Method and system for deskewing parallel bus channels to increase data transfer speed	HAQ, EJAZ UL
60295347	Not Issued	159	06/01/2001	System and method for increasing signal strength at a receiver in transmission lines with high attenuation	HAQ, EJAZ UL
07469617	Not Issued	161	01/24/1990	INTERBLOCK DISPERSED-WORD MEMORY ARCHITECTURE	HAQ, EJAZ UL
07505730	5126970	150	04/06/1990	STATIC RANDOM ACCESS MEMORY WITH PMOS PASS GATES	HAQ, EJAZ UL
08009475	5343438	150	02/01/1993	SEMICONDUCTOR MEMORY DEVICE HAVING A PLURALITY OF ROW ADDRESS STROBE SIGNALS	HAQ, EJAZ UL
08068547	5446697	150	05/28/1993	SEMICONDUCTOR MEMORY DEVICE	HAQ, EJAZ UL
08511812	5889719	150	08/07/1995	SEMICONDUCTOR MEMORY DEVICE	HAQ, EJAZ UL
08511815	5610869	150	08/07/1995	SEMICONDUCTOR MEMORY DEVICE	HAQ, EJAZ UL
08781953	5845108	150	12/19/1996	SEMICONDUCTOR MEMORY DEVICE USING ASYNCHRONOUS SIGNAL	HAQ, EJAZ UL
09057158	6160423	150	04/07/1998	HIGH SPEED SOURCE SYNCHRONOUS SIGNALING FOR INTERFACING VLSI CMOS CIRCUITS TO TRANSMISSION LINES	HAQ, EJAZ UL
09165705	6151648	150	10/02/1998	HIGH SPEED BUS SYSTEM AND METHOD FOR USING VOLTAGE AND TIMING OSCILLATING REFERENCES FOR SIGNAL DETECTION	HAQ, EJAZ UL
09318690	6430606	150	05/25/1999	HIGH SPEED SIGNALING FOR INTERFACING VLSI CMOS CIRCUITS	HAQ, EJAZ UL
09475087	6255859	150	12/30/1999	HIGH SPEED SOURCE SYNCHRONOUS SIGNALING FOR INTERFACING VLSI CMOS CIRCUITS TO TRANSMISSION LINES	HAQ, EJAZ UL

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Inventor Name Search Result

Your Search was:

Last Name = SLAGER
First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06328973	Not Issued	161	12/09/1981	INTERFACE BETWEEN A MICROPROCESSOR AND A COPROCESSOR	SLAGER, JAMES
06615081	4547849	150	08/17/1984	INTERFACE BETWEEN A MICROPROCESSOR AND A COPROCESSOR	SLAGER, JAMES
06110638	4259243	150	01/09/1980	PROCESS FOR THE PREPARATION OF 2,5-DIOXO-3H, 6H-FURO(3,2-B)FURAN-3A, 6A-DIACETIC ACID	SLAGER, JAMES E.
10086594	Not Issued	30	02/27/2002	Method and system for deskewing parallel bus channels to increase data transfer rates	SLAGER, JAMES R.
10159488	Not Issued	41	05/31/2002	System and method for increasing signal strength at a receiver in transmission lines with high attenuation	SLAGER, JAMES R.
60272222	Not Issued	159	02/27/2001	Method and system for deskewing parallel bus channels to increase data transfer speed	SLAGER, JAMES R.
60295347	Not Issued	159	06/01/2001	System and method for increasing signal strength at a receiver in transmission lines with high attenuation	SLAGER, JAMES R.

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